



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION  
HELD IN MAY 2019  
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3101 P R No : 201401159 Sex : M Name : KSHIRSAGAR MADHAV ALIAS PRASAD  
No Of Attempts : 2

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BB +	
	IA	2	AA +	
Digital VLSI Design	Theory	4	BB +	
	IA	2	AB +	
Analog VLSI Design	Theory	4	BC +	
	IA	2	AA +	
VLSI Technology	Theory	4	CC P	
	IA	2	BC +	
Advanced Engineering Mathematics	Theory	4	AB +	
	IA	2	BC +	
HDL - LAB	IA	2	AA +	
	Practical	2	AA +	
VLSI CAD - LAB	IA	2	AA +	
	Practical	2	AA +	
Total :		38		7.37 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *[Signature]*  
Checked By : *[Signature]*  
Date : 30/8/19

*[Signature]*  
30/8/19  
Aniketh Gaonkar  
Assistant Registrar-E(Prof.)

*[Signature]*  
30/8  
Prof. Anuradha Wagle  
Controller Of Examinations

*[Signature]*  
30/8  
Prof. Y. V. Reddy  
Registrar



799  
10/09/2019

Anand / NR / WB / HEICD  
A